

# **CORRECT AND EFFICIENT** ACCELERATOR PROGRAMMING



### Accelerators

#### Many cores on a single chip



### GPUs, e.g. from NVIDIA, AMD, ARM, offer high performance per unit power

# The Challenge

#### **Accelerator programming is hard!**

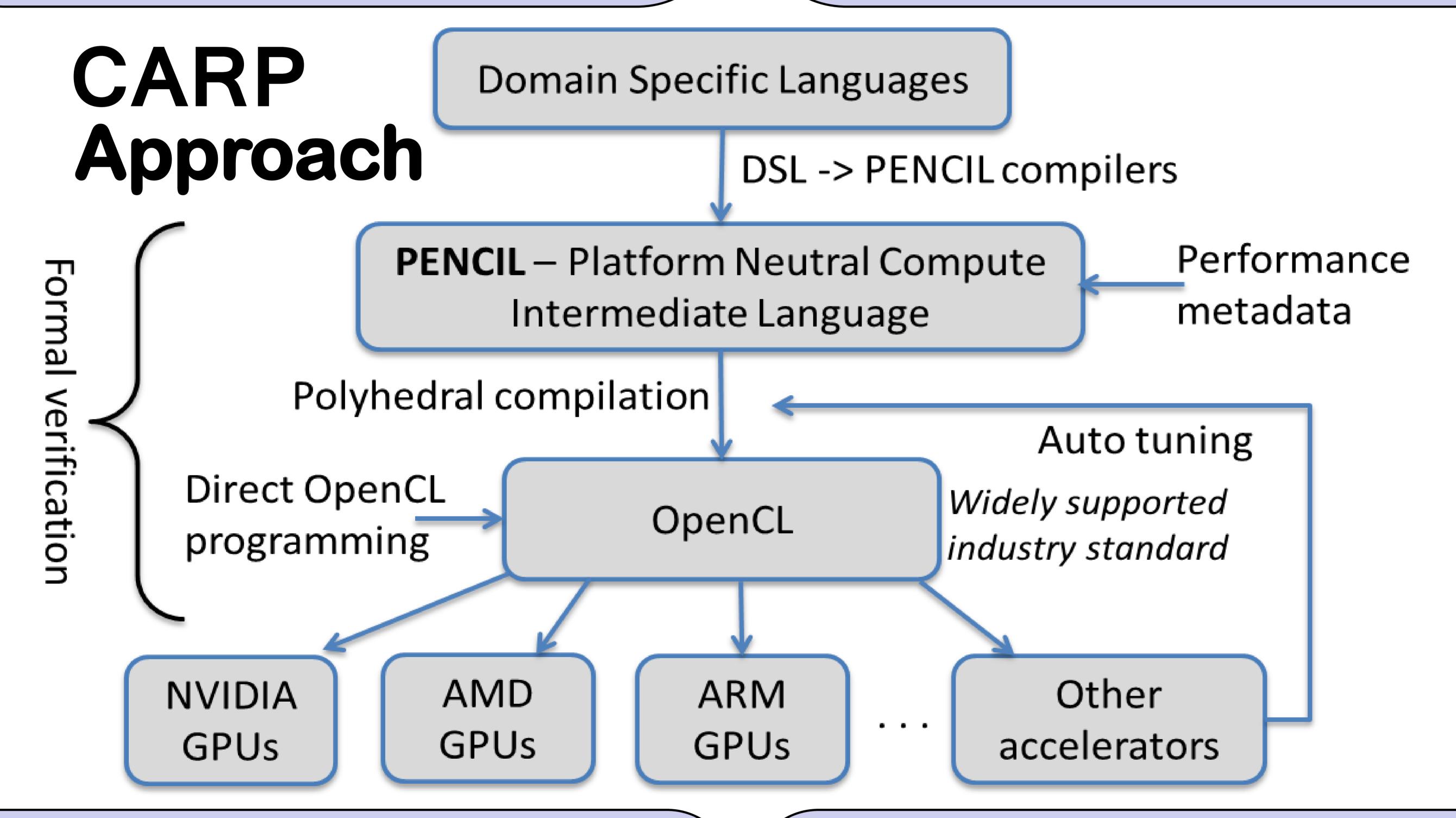
Difficult to achieve **portability** and **correctness** in low level languages like CUDA and OpenCL

#### **Problems:**

High cost of software development

Can beat CPU performance by orders of magnitude (execution time and energy consumption)

**Optimisation** for *diverse* platforms Maintenance of *multiple* sources **Correctness** across *all* platforms



## **CARP** Vision

Attack accelerator programming from top down:

**Higher-level programming model** and optimising compilers

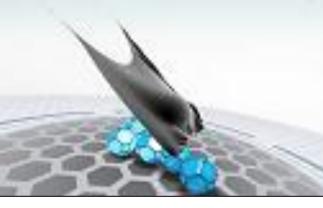
and **bottom up**:

**Formal verification techniques** 

## Demonstration

**Real time eye tracking:** GPUaccelerated across multiple platforms **Accelerator benchmarks:** Verification used to identify defects Linear algebra libraries: Portable





0 2 5 2

.4 0 0 0

#### for low-level code

performance



2

### **Expected Impacts Generated vs. hand-optimised code**:

**Software development productivity:** 

order of magnitude improvement

competitive performance

Fast, reliable and energy

efficient computer systems

#### www.carpproject.eu Find out more:

**Project no: 287767** Started: December 2011 Duration: 3 years

